

ISSUE CLASSIFICATION

PATENT NUMBER

## **U.S. UTILITY Patent Application**

 O.I.P.E. <i>SAW</i> o.a. <i>AA</i>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/552085	F	327	382	2816	Long Nguyen

## APPlicants

Tsutomu Ishikawa  
Hirosi Kojima

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Semiconductor integrated circuit having pads with less input signal attenuation

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) (Date)				
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner) (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner) (Date)				

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